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| APPLICATION NUMBER | FILING or 371(c) DATE | GRP ART UNIT | FIL FEE REC'D | ATTY.DOCKET NO | TOT CLAIMS | IND CLAIMS |
|-----------------------|--------------------------|-----------------|---------------|----------------|------------|------------|
| 10/039,707 | 11/09/2001 | 2111 | 990 | TALS0063 | 19 | 4 |

CONFIRMATION NO. 5641

CORRECTED FILING RECEIPT



OC000000034514629

Date Mailed: 02/12/2009

75671

Sadler, Breen, Morasch & Colby, ps
422 W. Riverside Ave, Suite 424
Spokane, WA 99201

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 75671

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/04/2002

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/039,707**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Method for increasing peripheral component interconnect (PCI) bus throughput via a bridge for memory read transfers via dynamic variable prefetch

Preliminary Class

710

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